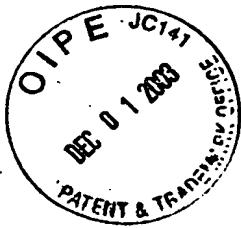


Fig. 1



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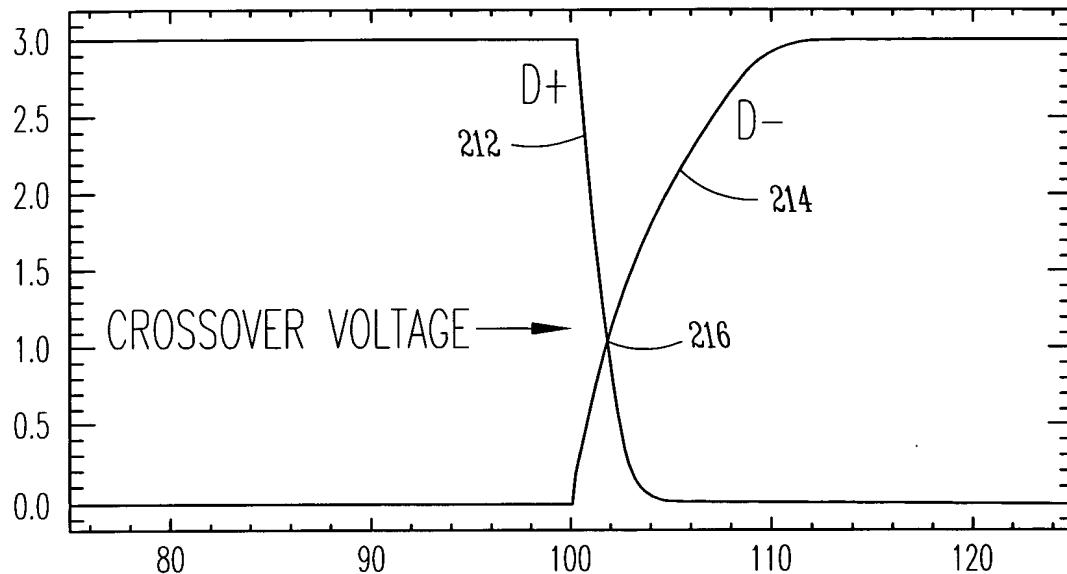


Fig. 2A

220

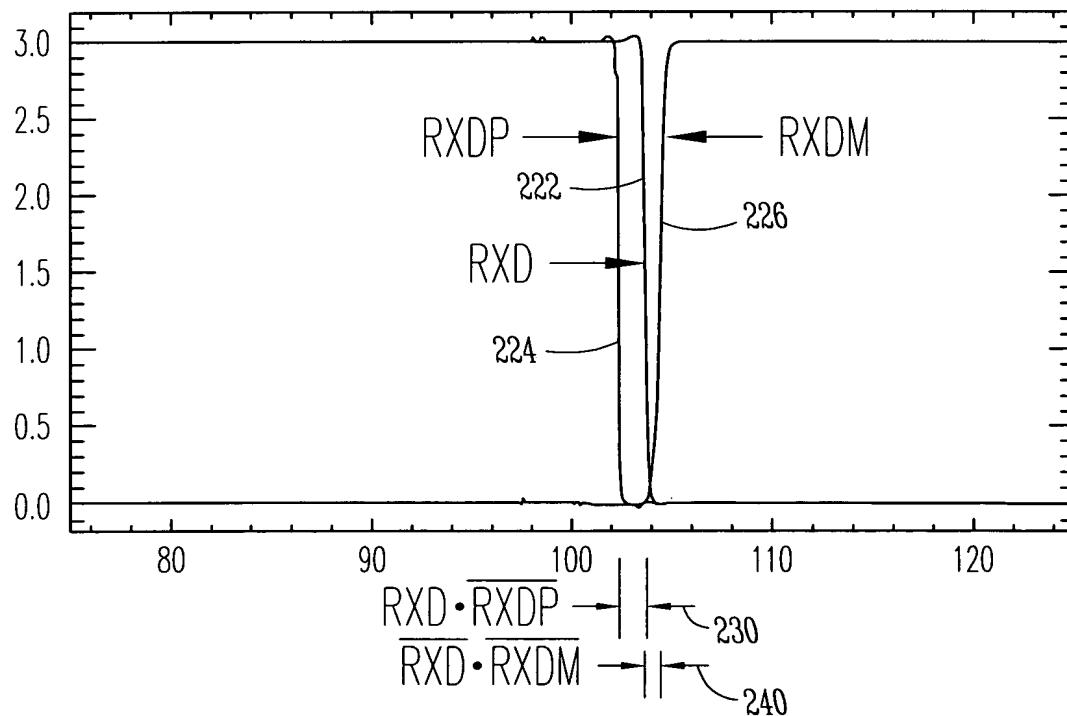
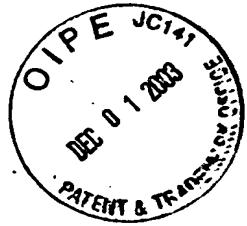


Fig. 2B



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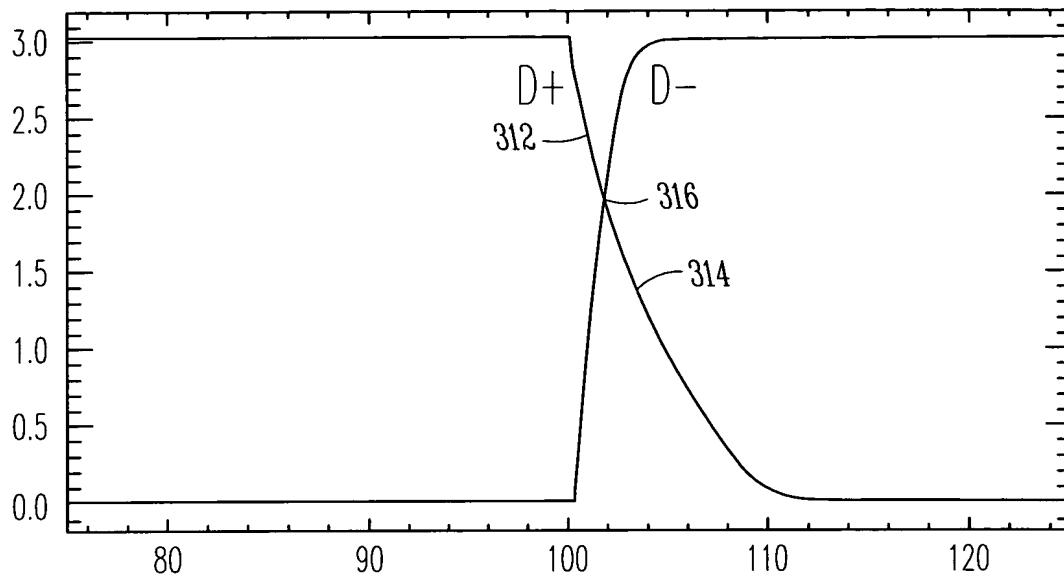


Fig. 3A

320

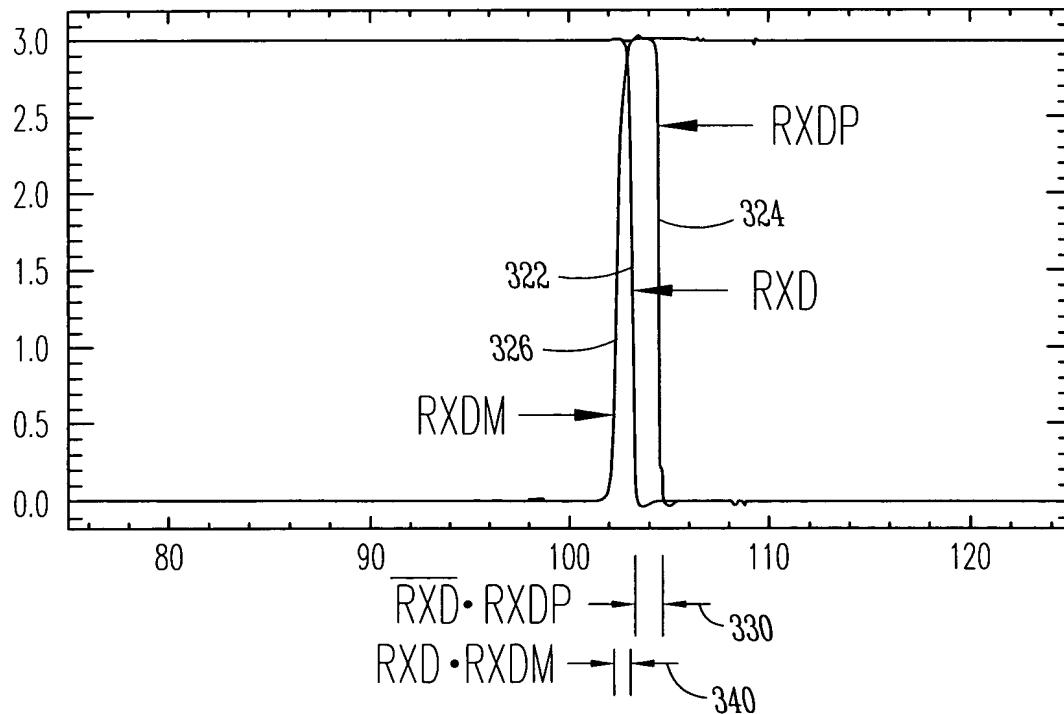
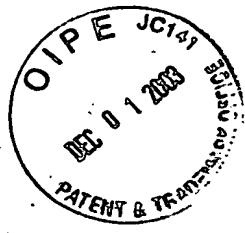


Fig. 3B



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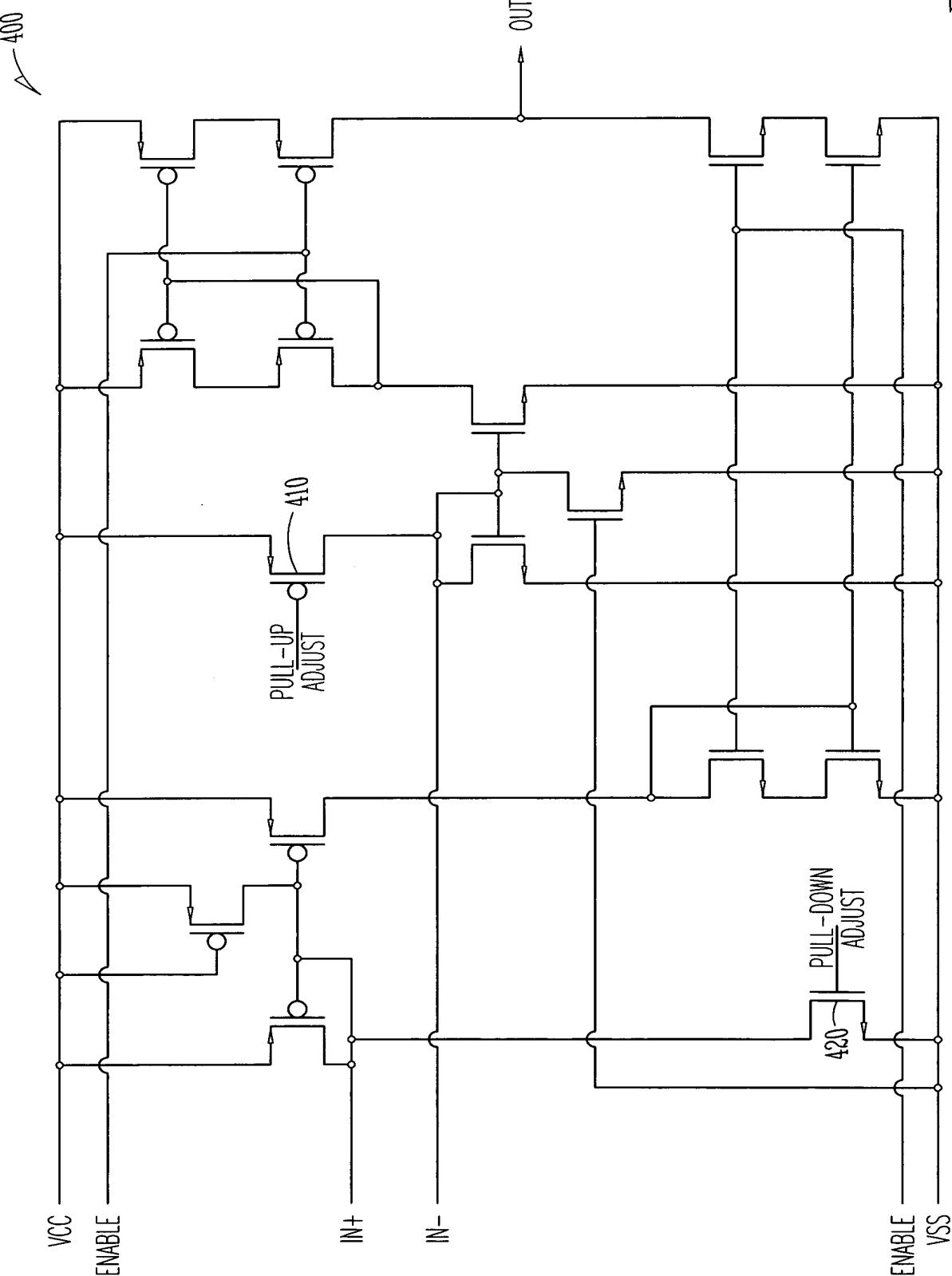


Fig. 4



TITLE: CROSS-OVER VOLTAGE LOCK FOR DIFFERENTIAL OUTPUT DRIVERS
INVENTORS NAME: James D. Patterson
SERIAL NO.: 10/612290

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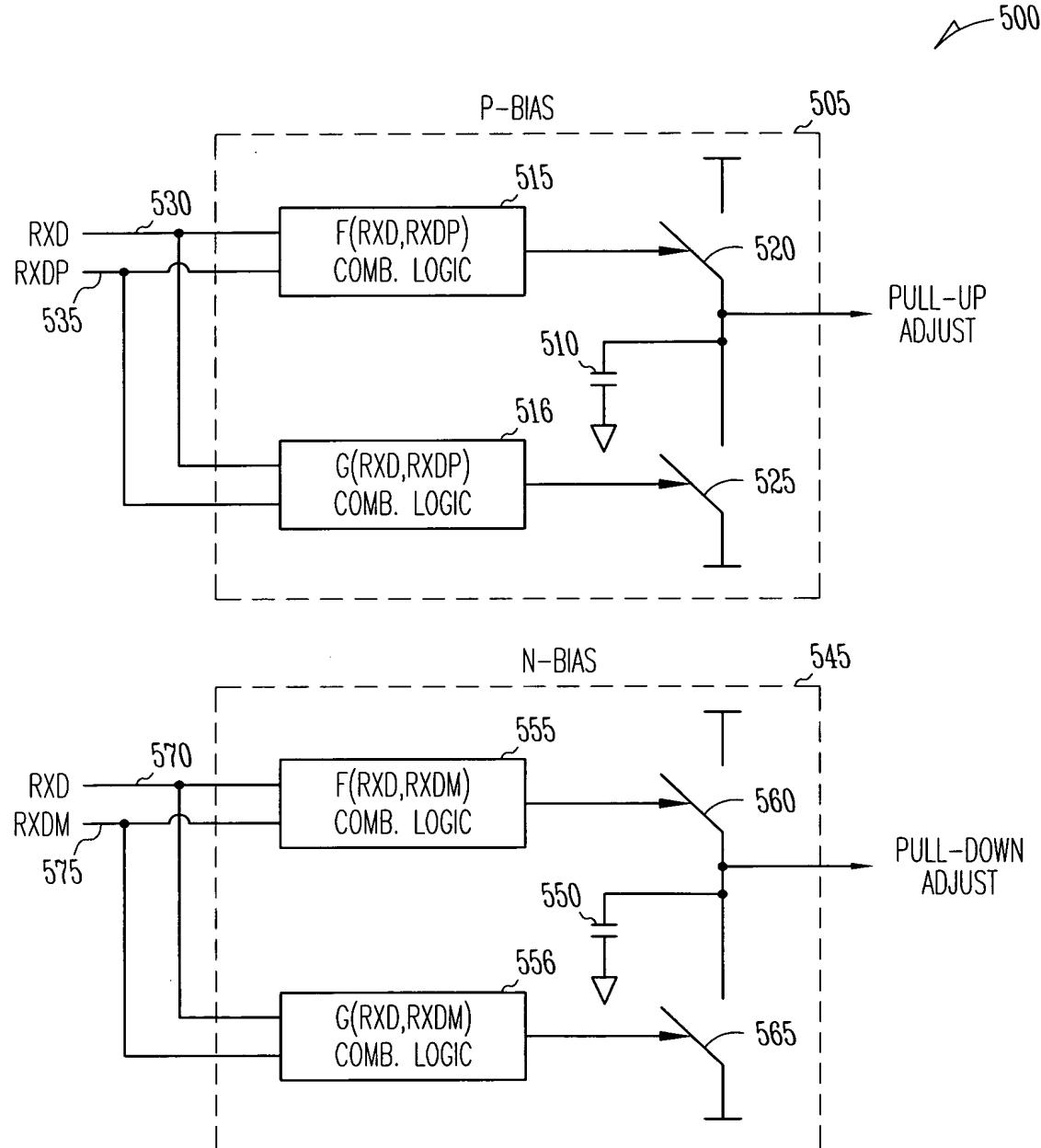
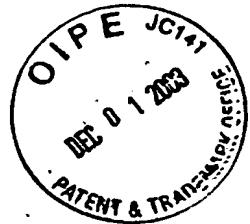


Fig. 5



TITLE: CROSS-OVER VOLTAGE LOCK FOR DIFFERENTIAL OUTPUT DRIVERS
INVENTORS NAME: James D. Patterson
SERIAL NO.: 10/612290

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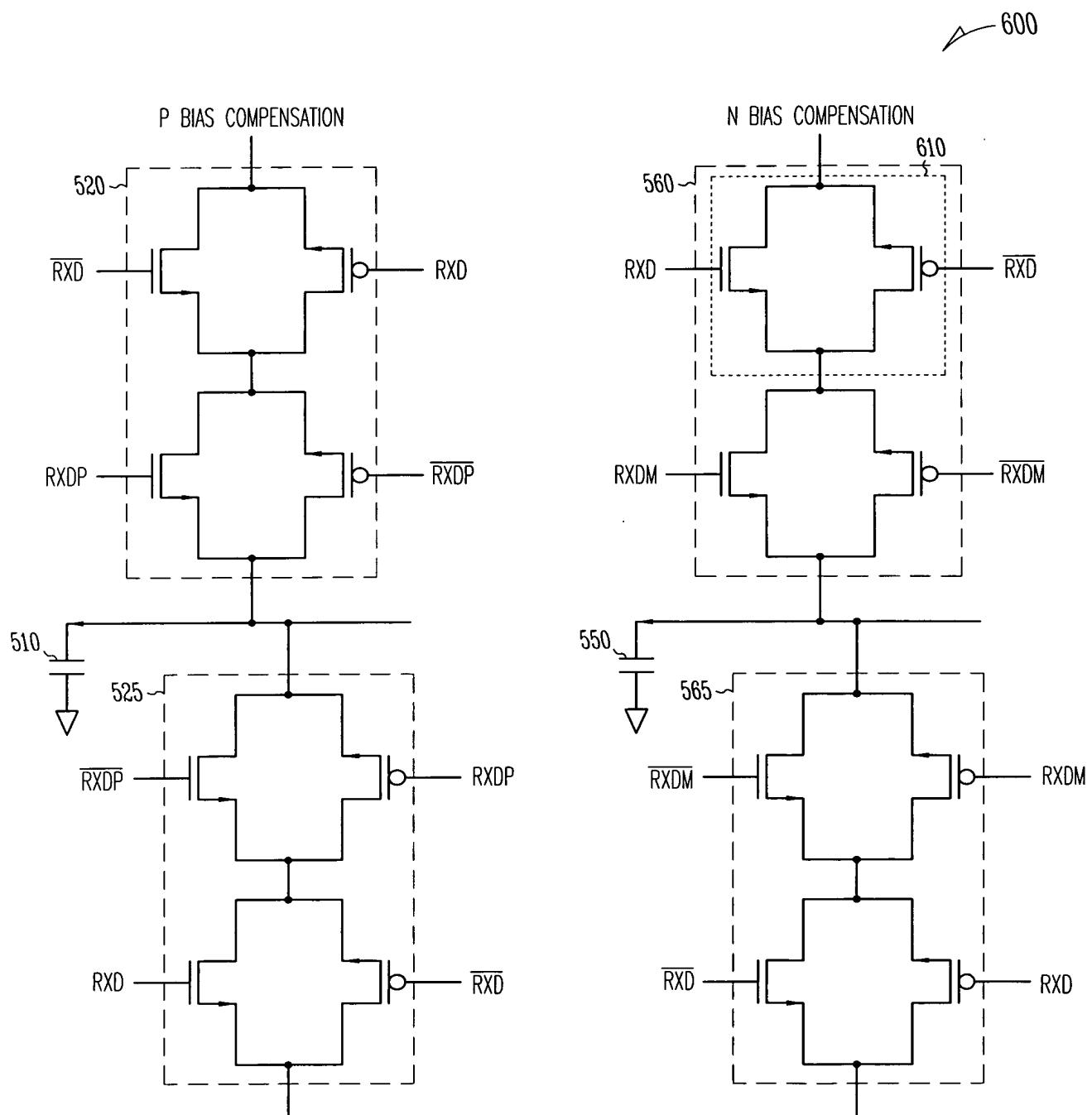
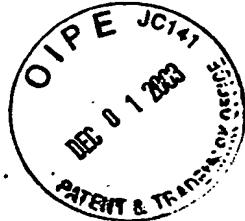


Fig. 6



TITLE: CROSS-OVER VOLTAGE LOCK FOR DIFFERENTIAL OUTPUT DRIVERS
INVENTORS NAME: James D. Patterson
SERIAL NO.: 10/612290

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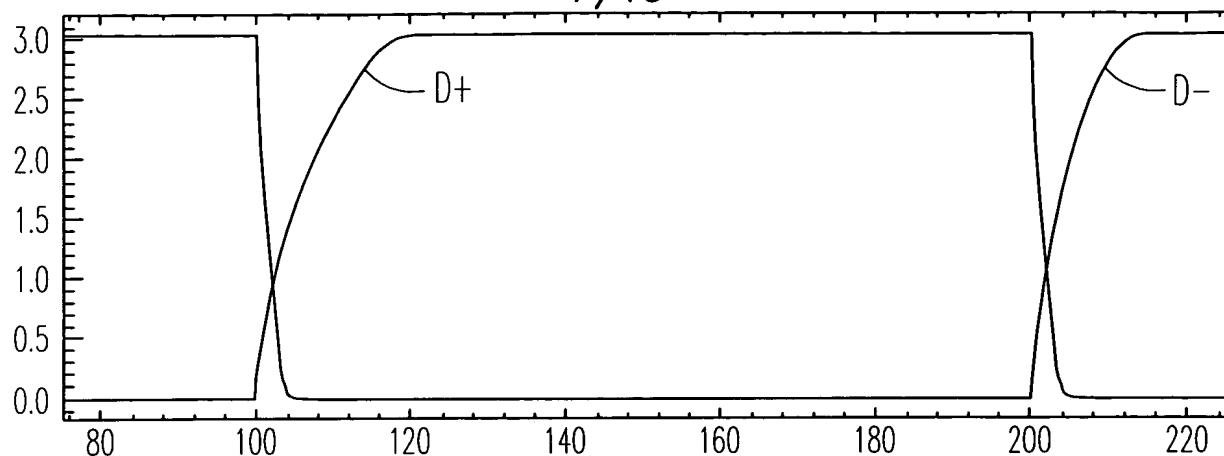


Fig. 7A

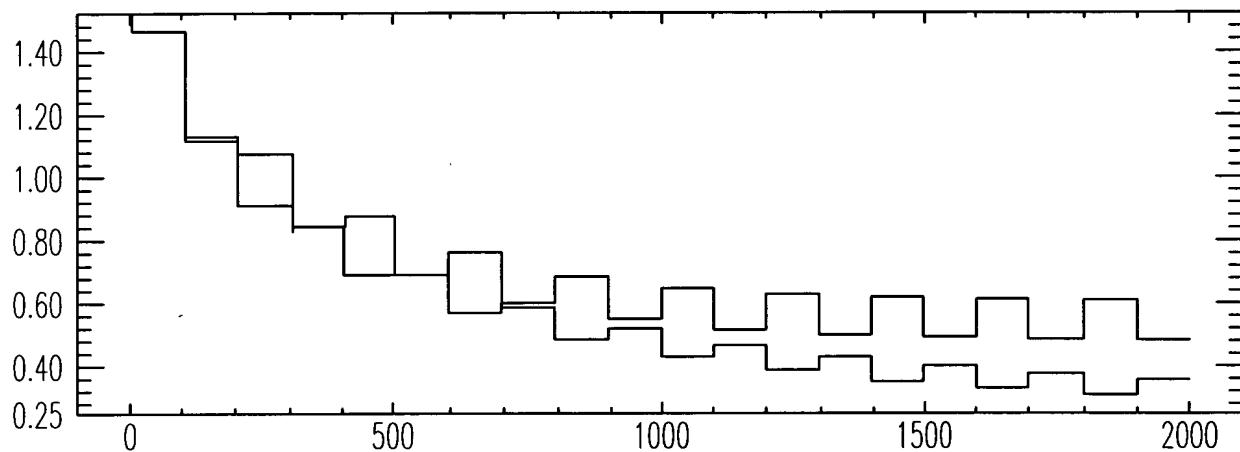


Fig. 7B

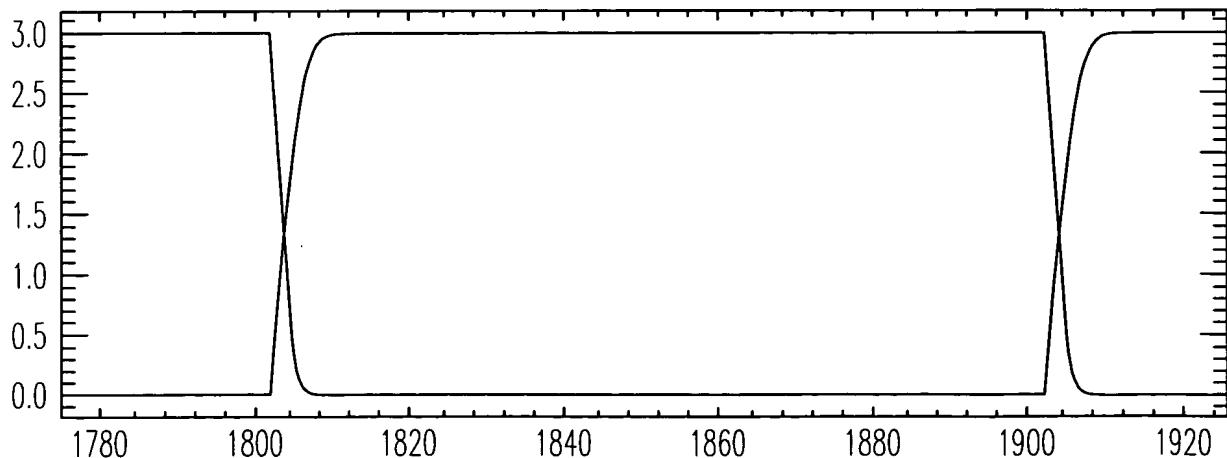
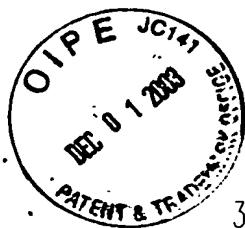


Fig. 7C



TITLE: CROSS-OVER VOLTAGE LOCK FOR DIFFERENTIAL OUTPUT DRIVERS
INVENTORS NAME: James D. Patterson
SERIAL NO.: 10/612290

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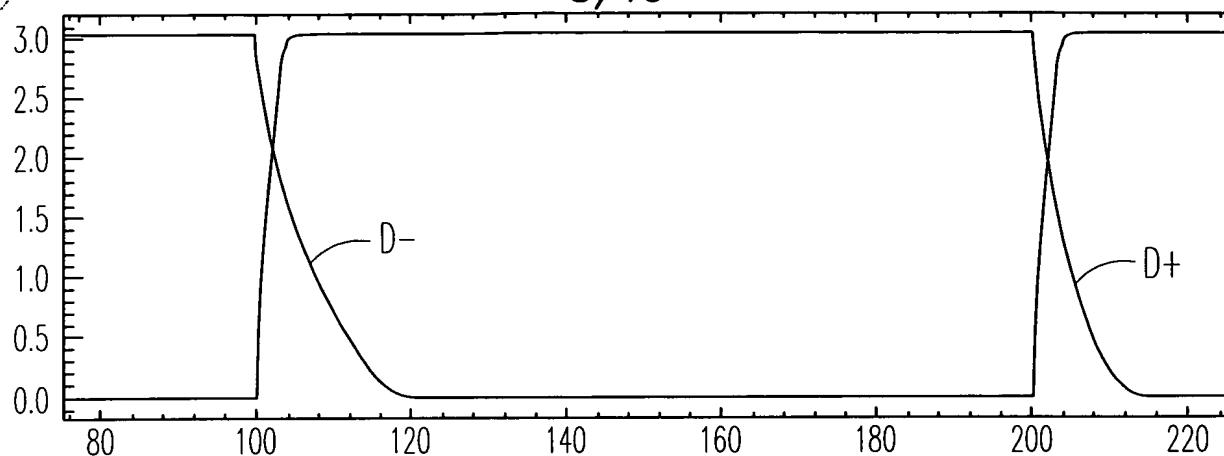


Fig. 8A

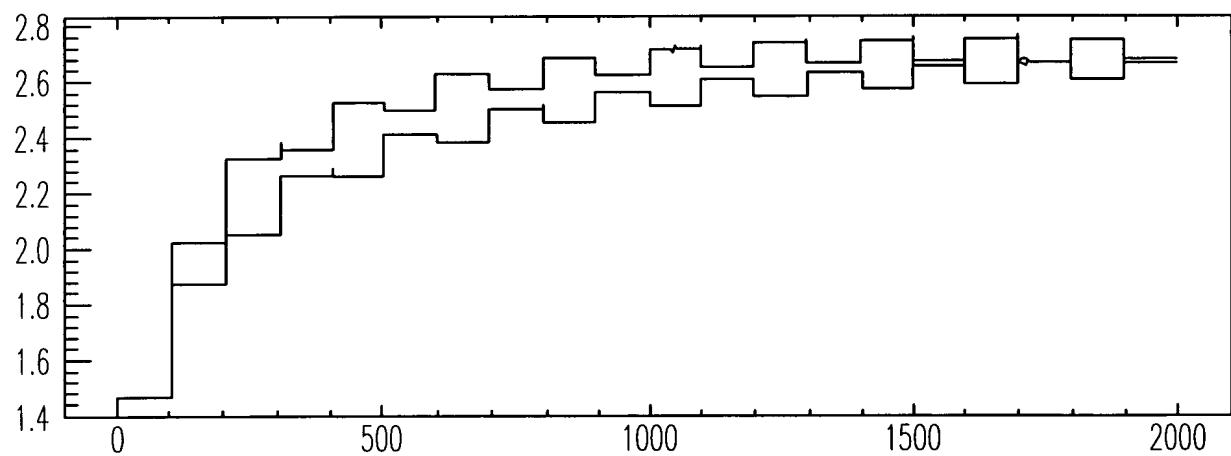


Fig. 8B

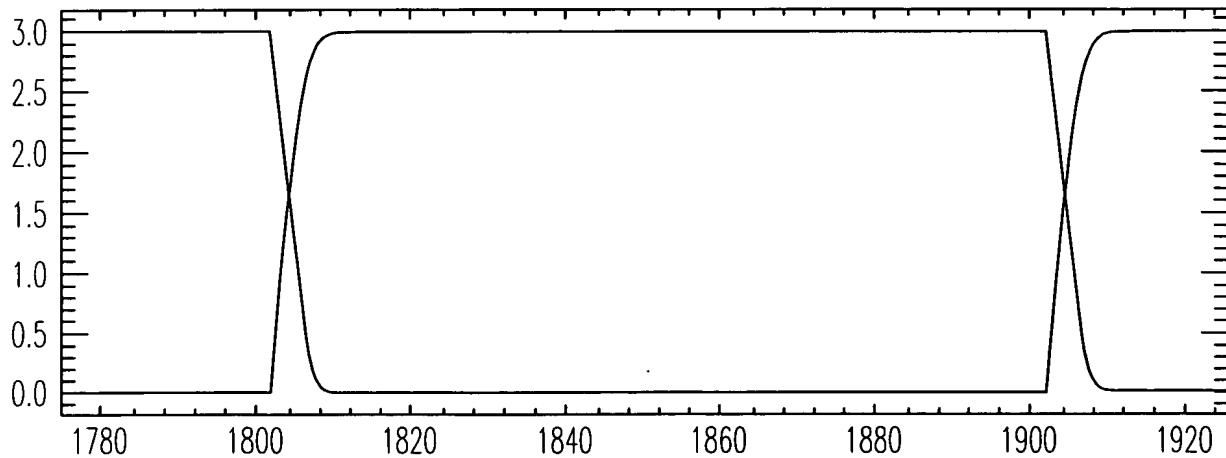
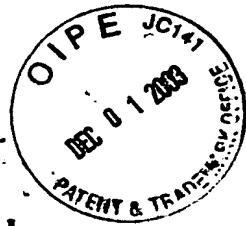


Fig. 8C



TITLE: CROSS-OVER VOLTAGE LOCK FOR DIFFERENTIAL OUTPUT DRIVERS
INVENTORS NAME: James D. Patterson
SERIAL NO.: 10/612290

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MEASURING A DIFFERENCE BETWEEN A VOLTAGE AT WHICH OUTPUT VOLTAGES OF FIRST AND SECOND DIFFERENTIAL DRIVERS OF A DIFFERENTIAL SIGNAL TRANSCEIVER CROSS-OVER AND A POINT SUBSTANTIALLY EQUIDISTANT BETWEEN MAXIMUM AND MINIMUM OUTPUT VOLTAGES

1020

PROVIDING A CORRECTING BIAS VOLTAGE PROPORTIONAL TO A DIFFERENCE BETWEEN THE CROSS-OVER VOLTAGE AND THE EQUIDISTANT VOLTAGE

1030

APPLYING THE CORRECTING BIAS VOLTAGE TO THE DIFFERENTIAL DRIVERS TO VARY THE POINT WHERE THE FIRST AND SECOND VOLTAGES CROSS-OVER

Fig. 10